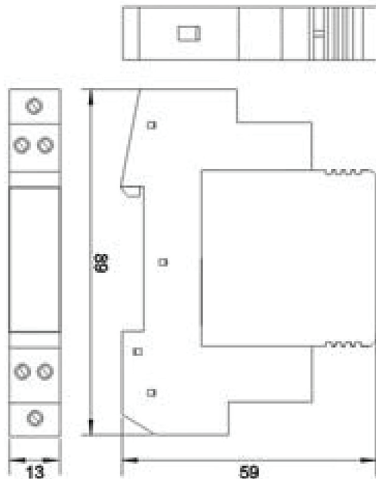


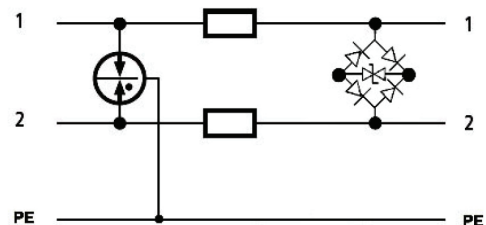
Surge arrester for double-wire data systems, protects against surges at the boundaries from lightning protection zone 0>3.

- Data network protector in accordance with IEC 61643-21:2000+A1:2008
- Standard design for double-wire systems
- Limit the transients with gas discharge tubes and Transzorb® diodes
- Three-stage protection circuit
- Plastic housing
- Terminal block connector base
- Simple installation

Dimensions



Basic Circuit Diagram



Technical Data

TYPE	ASIDM12-B0	ASIDM24-B0	ASIDM48-B0	
Nominal Voltage (V) U_n DC	12 V	24 V	48 V	
Max. Continuous Operating Voltage (V) U_c DC	15 V	28 V	60 V	
Nominal Discharge Current (8/20 μ s, kA) I_n	5 kA			
Nominal Discharge Current (8/20 μ s, kA) I_{nmax}	10 kA			
Nominal Current (A) I_L	0.5 A			
Voltage Protection Level (8/20 μ s, V) U_p	Line-Line	≤ 45	≤ 55	≤ 190
	Line-Ground	≤ 500	≤ 500	≤ 500
Voltage Protection Level (1 kV/ μ s, V) U_p	Line-Line	≤ 38	≤ 48	≤ 145
	Line-Ground	≤ 600	≤ 600	≤ 600
Transmission Speed V_s (bps)	2 M bps			
Series Impedance Per Line (Ohm)	2.2 Ohm			
Insertion Loss (dB)	≤ 0.5			
Protection Line	1 Twisted-Pair			
Operating Temperature Range	-40°C...+80°C			
Mounting	35 mm DIN Rail in accordance with EN50022/DIN 46277-3			

